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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/077,135	02/15/2002	Daryl Carvis Cromer	RPS9 2001 0156	2917

45503 7590 01/18/2006

DILLON & YUDELL LLP  
8911 N. CAPITAL OF TEXAS HWY.,  
SUITE 2110  
AUSTIN, TX 78759

EXAMINER
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PERUNGAVOOR, VENKATANARAY

ART UNIT	PAPER NUMBER
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2132

DATE MAILED: 01/18/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	Application No. 10/077,135	Applicant(s) CROMER ET AL.	
	Examiner Venkatanarayanan Perungavoor	Art Unit 2132	

**-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --**

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 19 December 2005.
- 2a) ☒ This action is **FINAL**.                      2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 1-3,5-12,14-21 and 23-26 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.
- 6) ☒ Claim(s) 1-3,5-12,14-21 and 23-26 is/are rejected.
- 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 15 December 2002 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
     Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
     Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All    b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- |  |   |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892)   | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

## **DETAILED ACTION**

### ***Response to Arguments***

1. The Applicant's arguments filed on 12/19/2005 regarding Claim 1 are not persuasive. As Sakaki et al.(hereinafter Sakaki) in U.S. Patent 5,826,007 discloses the persistent enable flag that is write-accessible only in response to detected power-on reset see Col 3 Ln 21-40 & Col 5 Ln 15-20 & Col 4 Ln 49-56(Sakaki mentions the monitoring of flag after power-on reset has been turned on and further the preventing of rewriting and write accessible only when /CE and /WR are enabled).
2. The Applicant's arguments regarding Claim 9 and 18 are not persuasive. As Sakaki discloses the in response to power-on reset determining the state of flag that is accessible by runtime programs to set next intended state of flag that enables or disables access to devices and setting/resetting the flag see Col 4 Ln 58-65 & Col 5 Ln 43-47.
3. The Applicant's amendments of Claims 1,9, and 18, namely adding "the persistent enable flag is read-only accessible to runtime program instructions" is also disclosed by Sakaki see Col 4 Ln 43-56(where Sakaki discloses the /WR input being used for write and read access to flag).

4. The Applicant's amendment to Claims 18-21 and 23-26 have obviated the 35 USC § 101 rejections, and are thus withdrawn.
5. The Applicant's amendment of the specifications have obviated the specifications objection and have thus been withdrawn.
6. The text of those sections of Title 35, U.S. Code not included in this action can be found in a prior Office action.

***Response to Amendment***

***Claim Rejections - 35 USC § 102***

7. Claims 1-3, 5-12, 14-21, and 23-26 are rejected under 35 U.S.C. 102(b) as being anticipated by U.S. Patent 5,826,007 to Sakaki.
8. Regarding Claim 1, Sakaki discloses the enable flag(Fig.1 item S1) being used to control access to a device(memories Fig.1 item 17 & 12) see Col 5 LL 24-33; a persistent flag being used to control access to device on subsequent power-on reset(Fig.1 item S2) see Col 5 Ln 43- Col 6 Ln 7. And the persistent enable flag that is write-accessible only in response to detected power-on reset see Col 3 Ln 21-40 & Col 5 Ln 15-20 & Col 4 Ln 49-56(Sakaki mentions the monitoring of flag after power-on reset has been turned on and further the preventing of rewriting and write accessible only when /CE and /WR are enabled); "the persistent enable

flag is read-only accessible to runtime program instructions” is also disclosed by Sakaki see Col 4 Ln 43-56(where Sakaki discloses the /WR input being used for write and read access to flag).

9. Regarding Claim 2, Sakaki discloses the power-on reset signal used for detecting power applied to power input see Col 6 Ln 26-45.
- 10.Regarding Claim 3, Sakaki discloses the determining of state of pending state change flag and determining of next state of enable flag in accordance with pending state change flag see Col 5 Ln 15-30.
- 11.Regarding Claim 5 and 6, Sakaki discloses the flag being non-volatile storage devices security device includes memory for receiving and storing data see Col 3 Ln 7-40.
- 12.Regarding Claim 7 and 8, 25 and 26, Sakaki discloses controlling access to security system and further enabling/disabling of devices see Col 6 Ln 26-36 & Col 5 Ln 58-65.
- 13.Regarding Claim 9, 18, Sakaki substantially discloses the setting/resetting of persistent and pending flags executed through runtime instructions see Col 5 Ln 21-30 and further the in response to power-on reset determining the state of flag

that is accessible by runtime programs to set next intended state of flag that enables or disables access to devices and setting/resetting the flag see Col 4 Ln 58-65 & Col 5 Ln 43-47.

14.Regarding Claim 10, 19, Sakaki discloses an input/output terminal for controlling access to memories and setting of flag see Col 5 Ln 34-42.

15.Regarding Claim 11 and 12, 20 and 21, Sakaki discloses the enable and disabling flag and controlling access being set after power-on reset see Col 5 Ln 15-20 & Ln 48-62.

16.Regarding Claim 14, 16, 17, 23, Sakaki discloses controlling access to security system and further enabling/disabling of devices see Col 6 Ln 26-36 & Ln 58-65.

17.Regarding Claim 15, 24 Sakaki discloses memory for receiving and storing data see Col 5 Ln 34-42.

### ***Conclusion***

18. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a). A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date

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of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

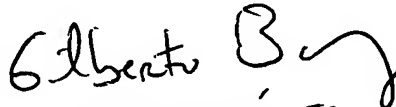
19. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Venkatanarayanan Perungavoor whose telephone number is 571-272-7213. The examiner can normally be reached on 8-4:30. If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Gilberto Barron can be reached on 571-272-3799. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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20. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Venkatanarayanan Perungavoor  
Examiner  
Art Unit 2132

VY  
1/10/2005

  
GILBERTO BARRÓN JR.  
SUPERVISORY PATENT EXAMINER  
TECHNOLOGY CENTER 2100